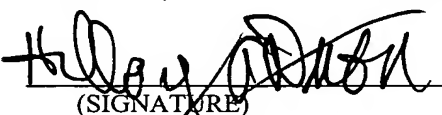


16085 U.S. PTO
020504

PATENT APPLICATION
Docket No. 8750-057
Client No. SPX200303-0009US

31353 U.S. PTO
10/773805
020504

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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I HEREBY CERTIFY THAT THIS PAPER AND ENCLOSURES AND/OR FEE ARE BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE "EXPRESS MAIL POST OFFICE TO ADDRESSEE" SERVICE UNDER 37 CFR 1.10 ON THE DATE INDICATED ABOVE AND IS ADDRESSED TO: MAIL STOP PATENT APPLICATION, COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450.	
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P.O. Box 1450
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Enclosed for filing is a patent application under 37 CFR 1.53(b) of:

Inventor(s): Sang-Eun Lee and Yun-Heub Song

For: METHOD OF MANUFACTURING A SEMICONDUCTOR
INTEGRATED CIRCUIT USING A SELECTIVE DISPOSABLE
SPACER TECHNIQUE AND SEMICONDUCTOR INTEGRATED
CIRCUIT MANUFACTURED THEREBY

Applicant requests FIG. 14A to be published with the application.

Enclosures:

- ☒ Specification (pages 1-16); claims (pages 17-26); abstract (page 27)
- ☒ TWENTY-NINE sheet(s) of FORMAL drawings (comprising 53 figures)
- ☒ Certified copy of Priority Document No. 2003-7547, filed February 6, 2003, from which priority is claimed.

CLAIMS AS FILED				
For	Number Filed	Number Extra	Rate	Basic Fee \$770
Total Claims	39-20	19	x \$ 18 =	\$342.00
Independent Claims	5-3	2	x \$ 86 =	\$172.00
TOTAL FILING FEE				\$1284.00-

- ☒ 1449 Information Disclosure Statement with 1 cited U.S. Patent (copy not enclosed)
☒ Combined Declaration and Power of Attorney
☒ Newly executed (original or copy)
☐ Assignment with cover sheet
Assignee Name and Address: Samsung Electronics Co., Ltd.
Suwon-city, Kyungki-do Republic of Korea
- ☒ Return Postcard
☒ PTO Form 2038 authorizing credit card payment of \$1284.00, filing fee (\$770.00) and additional claims fee (\$514.00) is enclosed.
☒ Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Customer No. 20575

Respectfully submitted,

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